In the following Cs means calibration step. N is the choice. (See B01610, B01030) At calibration, the following can be chosen for the serial outputs: Display Function. Described in B01040 or else noted. Display

- =0
- 8 bits, odd parity, 1 stop bit. =1

Cs07

Cs06

Cs13

- =2
- 8 bits, no parity, 1 stop bit. (Preset).
  8 bits, no parity, 2 stop bits.
  8 bits, even parity, 1 stop bit.
  7 bits, even parity, 1 stop bit. =3
- =4 7 bits, odd parity, 1 stop bit.
- =5
- 7 bits, even parity, 1 stop bit. 7 bits, odd parity, 2 stop bits. 7 bits, even parity, 2 stop bits. =6
- =7
- Serial input. Described in B01050. +8

Protocols for output on 15p D-sub (U1275 standard RS232, 9p D-sub, J6). Signal designation is 1. If printer output is chosen at both Cs6 and 13, only 6 is used.

- Output 2. Display and mode indicators. =0
  - Display (peak for Cs3:+4) in ASCII output. For printer. (Preset) =1
  - =2 Programmable continuous serial output of display. B00900.
  - =3 Display in ASCII. Print number reg. 59S (159S). For printer.
  - Weight BCD value in ASCII every measurement cycle. No mean value. =4
  - =5 Display in ASCII Net & gross. For printer.
  - =6 AD value in ASCII every measurement cycle. No mean value.
  - =7 Display in ASCII Net & gross. Print number reg. 59S (159S). For printer.
  - Output 1. Display, tare and mode indicators. =8
  - =10 Output 3. Display, tare, setpoints, mode indicators, 16 bits DA-value.
  - =15 No output.

Protocols for output on 25p Dsub. Signal designation is 2. (Input also on U1275, J6.) If non-printer output is chosen in both Cs6 and 13, only 13 is used.

- =0 Output 2. Display and mode indicators.
- =1 Display (peak for Cs3:+4) in ASCII output. For printer. (Preset)
- Programmable continuous serial output of display. B00900. =2
- Display in ASCII. Print number reg. 59S (159S). For printer. =3
- =4 Weight BCD value in ASCII every measurement cycle. No mean value.
- =5 Display in ASCII Net & gross. For printer.
- =6
- AD value in ASCII every measurement cycle. No mean value. Display in ASCII Net & gross. Print number reg. 59S (159S). For printer. =7
- =8 Output 1. Display, tare and mode indicators.
- Output 3. Display, tare, setpoints, mode indicators, 16 bits DA-value. Protocol 4. Bidirectional. Cs07:+8 is not needed. =10
- =11
- No output. =15

Protocols N=0, 2, 4, 6, 8 and 10 are sent out once every measurement cycle, but N=4, 6 and 8 are stopped, when the indicator is not in weighing mode.

The other (N=0, 2 and 10) follows the display in non weighing mode, but input speed from keyboard is maximum once per measurement cycle (80ms).

Register 59S (159S) increments one step for every print command.

Cs27 =1-99 Address number for the serial input. N=0 means no address.

Double width means that S0 = ASCII 14 (hex 0E) is sent at the start of each text.

- Cs08 =0 Swedish text.
  - Print in double width (Some printers). ASCII 14 (SO) is sent out. +1
  - Mostly no normal printer output. Used with e.g. Cs11:3 or for high speed. +2
  - +4 English text.
  - +8 Print output only if addressed according to Cs27. B01050
- =0-15 Cs14 Linefeeds between printings. CR = 13 (from 1996, CR + LF) is used . (Preset to 1).
- Cs22 =0-99 Steps tabulation from left margin at printing.

For printers with no character buffer after DTR is set low.

Cs10 +1 100ms delay after CR and LF for some printers with line buffer only.

Number of measurement cycles and serial outputs per measurement cycle: Cs09

- =0 Display every 4 measurement cycle.
- =1
- =2
- Display every measurement cycle. Display & output (Cs6&13, 13=0, 2 or 10), every 4 measurement cycle. Display & output (Cs6&13, 13=0, 2 or 10), every 16 measurement cycle. =3

A 6 digit number may be entered more than one time between each weight print. Cs01

- Entering 6 digit and {ENTER} stores the number in 24L and prints the number and the weight. 6 digit and {ENTER} stores the number in 24L but prints only number. =0
- +2

After each print command the scale must be unloaded to zero range for new print if: Cs04 +2 Unload to zero range before new print.

Print command is inhibited at motion or may be delayed until motion disappears by:

Cs19 =0 No printing at motion.

+2 Printing when motion disappears.

There are also possibility to store arbitrary texts, commands and registers (up to 100 ASCII characters and 24 string, each with 20 characters), in the battery backup RAM and later print these on command. Cs11:=3. Described in DB00900.

Timeout, when DTR is low, is 255 measuring cycles (normally 80ms) or 20 seconds.

In calibration mode, when {ZERO} is pushed (BCD value displayed) and then {L}, all calibration values are sent out. Space is printed F.

When ASCII for printer is chosen, data are sent to the printer after {ENTER}, {TEST}, {T}, {S} or {L}. At the end of all printer ASCII lines CR = 13 (from 1996, CR + LF) is sent out.

### Examples when ASCII text for printers is chosen.

Command:	Printer.		Comment.
{ENTER} Cs6:=1 Cs13:=1	+222.22 kg Gross		Prints weight.
{ENTER} Cs6:=7 Cs13:=7	Nr +00001		Prints net and gross weight and number
	+111.11 kg Net		of printing.
	+222.22 kg Gross		
{y}{y}{S}	+222.22 kg 00001#	Add yyS	0≤ yy≤ 49 Adds in sum register yyS.
{y}{y}{T}	+111.11 kg	Tare yyT	0≤ yy≤ 99 Tare with weight in yyT.
{R}{y}{y}{S}{ENTER}	+12345678.90 kg	yyS	$0 \le yy \le 49$ Print weight in sum register
	+zzzzz #	1yyS	yyS and number of sums in 1yyS.
{R}{y}{y}{T}{ENTER}	+111.11 kg	ууТ	0≤ yy≤ 99 Print tare register yyT.
${R}{y}{y}{L}{12345}{L}{67890}{R}{ENTER}$	+xxxxxxxxx.xx kg	yyL	0≤ yy≤ 7 Enter new setpoint in yyL.
	-12345678.90 kg	ууL	
{R}{y}{y}{S}{00024}{ENTER}	+xxxxxxxxx.xx kg	yyS	$0 \le yy \le 49$ Preset sum register yyS to
	+00024000.00 kg	yyS	24 000.00 kg. 1yyS is not changed.
	+ууууу #	1yyS	
{R}{y}{y}{S}{0}{ENTER}	+xxxxxxxxx kg	yyS	$0 \le yy \le 49$ Set yyS and 1yyS to zero.
	+ууууу #	1yyS	
	+00000000.00 kg	yyS	
	+00000 #	1yyS	
{C}{n}{L}	+xxxx.xx g	17L	Enter counting mode with n pieces.
{R}{1}{7}{L}{ENTER}	g/Unit +xxxx.xx		Print stored unit weight.
{S} in counting mode.	Units +xxxxx		Add number of pieces in countin mode.
	Nr +yyyyy		
	+ZZZZZZZZZZ		

{F}{x}{x}{ENTER} prints out xxx and register according to B00900.

The Baud rate is set with the switch on the digital board.

At delivery 1200 baud is set. Do not use higher baud rates then necessary. To work properly at high speed and high capacitive loads, the serial outputs must be loaded with 3 to 5mA.

E.g.: At 5V, 1k <	Rload	<	1.7k.	At	12V,
2.4k < Rload < 4k.					
		• .			

If normal RS232 output is wanted, serial transmitter U13252 can be used.

Optoisoplated standard RS232 with 25p D-sub in option U1290 (Cs13, signal 2). U1275 has also standard RS232 with 9p D-sub (see Cs6, signal 1).

Special serial outputs 1, 2 and 3. Common notations:

MOT Motion = 1 P2 P1 P0

OVL	Overload = 1	0	0	0 = no decimal poir	۱t.
		~	~		

ZER	Zero = 1	0	0	1	= XXXXX.
SGN	Sign - = 1	0	1	0	= XXXX.X
TAR	Tared = 1	0	1	1	= XXX.XX
		4	~	~	

NET Net = 1 1 0 0 = xx.xxx GRO Gross = 1 1 0 1 = x.xxxx INP Input "2" = 1 (U1272)

Dn:b means display digit n from 1 to 5 (5 is most significant digit) and b 1,2,4 or 8 in BCD value. In the same way for T, which means used tare value. When LT = 1, bit 3 to 0 in the BCD digits switch between 1000 (8) and 1111 (blanking).

Baud rate.

U1270/2/3

S1:1 on

S1:3 on

S1:5 on S1:7 on

S1:2 on

S1:4 on

S1:6 on

S1:8 on

U1274

S1:1 on

S1:2 on

S1:3 on

S1:4 on

\_

\_

U1275

JP13 on

JP12 on

JP11 on

JP10 on

JP13 on

JP12 on

JP11 on

JP10 on

Speed

4800 baud

2400 baud

1200 baud

4800 baud

2400 baud

1200 baud

300 baud

300 baud

Signal 1, CS06

\_"\_

\_"\_

Signal 2, Cs13

\_"\_

\_"\_

**Description of special serial output 1.** Cs6:=8 or Cs13:=8. Output every measurement cycle. Byte 1, bit 0-3 are always hexadecimal E=1110 for recognition.

Byte	bit 0	bit 1	bit 2	bit 3	bit 4	bit 5	bit 6	bit 7	
1	0	1	1	1	GRO	NET	INP	SGN	
2	2 D5:1	D5:2	D5:4	D5:8	D4:1	D4:2	D4:4	D4:8	
3	D3:1	D3:2	D3:4	D3:8	D2:1	D2:2	D2:4	D2:8	
4	D1:1	D1:2	D1:4	D1:8	ZER	TAR	OVL	MOT	
5	5 T5:1	T5:2	T5:4	T5:8	T4:1	T4:2	T4:4	T4:8	
6	5 T3:1	T3:2	T3:4	T3:8	T2:1	T2:2	T2:4	T2:8	
7	' T1:1	T1:2	T1:4	T1:8	0	P0	P1	P2	

**Description of special serial output 2.** Cs6:=0 or Cs13:=0 Output every measurement cycle, also non weighing mode. Bit 4-6 are address for e.g. UART.

Bit 4-6 are address for e.g. UART. DP Decimal point to the right of corresponding digit = 1. LT Lamp test = 1.

Byte	bit 0	bit 1	bit 2	bit 3	bit 4	bit 5	bit 6	bit 7
1	D5:1	D5:2	D5:4	D5:8	0	0	1	DP
2	D4:1	D4:2	D4:4	D4:8	1	1	0	DP
3	D3:1	D3:2	D3:4	D3:8	0	1	0	DP
4	D2:1	D2:2	D2:4	D2:8	1	0	0	DP
5	D1:1	D1:2	D1:4	D1:8	0	0	0	DP
6	GRO	NET	INP	SGN	0	1	1	LT
7	ZER	TAR	OVL	MOT	1	1	1	LT

**Description of special serial output 3.** Cs6:=10 or Cs13:=10. Output every measurement cycle, also non weighing mode.

Byte 1, bits 0-3 are always hexadecimal E=1110 for recognition.

Other bytes with the value 1110 for bit 0 to 3 in byte 1 are changed to F=1111 in the output.

DA-converter value is a 16 bit binary code and it is used for a 0-20mA analog output. All bits = 0 (0mA) corresponds to zero on the display minus 4 increments. If Cs28:+2 is chosen, 20% of full scale (4mA) corresponds to zero.

All bits = 1 (20mA) corresponds to full scale plus 3 increments.

If Cs28:+1 is chosen, the net (displayed) value is used, otherwise the gross (calibration) value is used.

WGH = 1 indicates net or gross display. (DA-value and setpoints are valid).

Byte	bit 0	bit 1	bit 2	bit 3	bit 4	bit 5	bit 6	bit 7	Comments
1	0	1	1	1	GRO	NET	INP	SGN	
2	D5:1	D5:2	D5:4	D5:8	ZER	TAR	OVL	MOT	
3	D4:1	D4:2	D4:4	D4:8	B0	B1	B2	B3	B0 - B15 binary weight
4	D3:1	D3:2	D3:4	D3:8	B4	B5	B6	B7	value for AD-converter.
5	D2:1	D2:2	D2:4	D2:8	B8	B9	B10	B11	B0 = LSB (Least Signi-
6	D1:1	D1:2	D1:4	D1:8	B12	B13	B14	B15	ficant Bit).
7	T5:1	T5:2	T5:4	T5:8	L0	L1	L2	L3	L0 - L7 setpoints for
8	T4:1	T4:2	T4:4	T4:8	L4	L5	L6	L7	sum registers S0 - S7.
9	T3:1	T3:2	T3:4	T3:8	L8	L9	L10	L11	L8 - L15 setpoint for
10	T2:1	T2:2	T2:4	T2:8	L12	L13	L14	L15	weight.
11	T1:1	T1:1	T1:4	T1:8	WGH	P0	P1	P2	-

If Cs28:+8 is chosen, the printer output in Cs6 or the programmable output (B00900) is sent out on both outputs after command. Before this, one transmission with P0 = P1 = P2 = 1 is sent out. When the printer transmission is ready and the indicator back in weighing mode, special output 3 is resumed after a delay of about one second.

# Description of special serial protocol 4. Cs13:=11.

This is used for bidirectional communication with a computer.

The serial input works without Cs07:+8.

The computer asks: <stx> [ASCII message] [BC</stx>	CC] <etx> where</etx>
<stx> is</stx>	(002) decimal ASCII value.
<etx> is</etx>	(003).
[ASCII message] is:	$\dot{PB} = (080) (066) = Gross weight request.$
	PN = (080) (078) = Net weight request.
	PT = (080) (084) = Tare request.
	DI = (068) (073) = Displayed weight request.
The indicator answers: <stx> [S1] [S2] [00www</stx>	ww] [BCC] <etx> where</etx>
[S1] is:	B = (066) = Gross weight.
	N = (078) = Net weight.
	T = (084) = Tare value.
	D = (068) = Displayed weight.

[S2] is + (043) or - (045). [00wwww] The weight in 5 digits and 2 leading zeros.

[BCC] means "Binary Check Control" and is computed as follows:

On the tramsmitted message an XOR is computed on all 8 bit values excluding <ETX>. This 8 bit value is [BCC].

On the received message an XOR is computed on all 8 bit values including [BCC] but excluding <ETX>. The result must be zero, else the message is skipped.

## **Connectors:**

00.	15p D-sub female. U1272/3. (Refers to Cs6, signal 1.)								
6	+TD1 Opto isolated output. Transmit Data. On (negative) at rest. <25V, <5mA, Von <1V.								
5	-TD1 Return for pin 5. DTR1 Opto isolated input, Data Terminal Ready, Negative polarity not ready, 5–9mA, 825ohm								
13	Opto isolated PRINT/TEST input, negative polarity. 5–9mA, 825ohm in series.								
14	Opto isolated TARE input, negative polarity. 5–9mA, 825ohm in series.								
15	Ground	pins 7,	13, 14	4, J1:1	6 and (J1:24, see below).				
8	0 (Digital zero).								
11	+5 Volt.								
2	W3 Spare.								
3	W4 Spare.								
9	W5 Spare.								
12	W7 Spare.								
16	DC222 interface On		mala	(Outr	ut refere to CoC, signal 1, input to Co12, signal 2)				
J6: 1	W10 Spare	D-Sub	male.	(Outp	ut refers to Cso, signal 1, input to CS13, signal 2.)				
2	TXD1 Outpu	it.		Trans	mit Data.				
3	RXD2 Input.			Recei	ve Data. Port 1.				
45	GND OV Di	it aital ze	ero	Data	i erminai Ready.				
6	W11 Spare	9. ion _ i							
7	W12 Spare			Cloar	To Sond Port 1				
9	W13 Spare			Cieai	To Send. Port 1.				
	· · · ·								
J1:	25p D-sub male		Cs3:=		Refers to Cs 13, signal 2.				
	connected to:	1	0	2,3					
8	connected to: PA Output, limit L	1 7	0 15	2,3 3	Synchronous output type 1. Cs2:+4.				
8 9	Connected to: PA Output, limit L PA Output, limit L	1 7 6	0 15 14	2,3 3 2	Synchronous output type 1. Cs2:+4. Clock for both synchronous outputs.				
8 9 10 11	Connected to: PA Output, limit L PA Output, limit L PA Output, limit L PA Output, limit L	1 7654	0 15 14 13 12	2,3 3 2 1 0	Synchronous output type 1. Cs2:+4. Clock for both synchronous outputs.				
8 9 10 11 12	Connected to: PA Output, limit L PA Output, limit L PA Output, limit L PA Output, limit L PA Output, limit L	1 76543	0 15 14 13 12 11	2,3 3 2 1 0 11	Synchronous output type 1. Cs2:+4. Clock for both synchronous outputs. Synchronous output type 2. Cs2:+8.				
8 9 10 11 12 13 18	Connected to: PA Output, limit L PA Output, limit L	1 7654321	0 15 14 13 12 11 10 9	2,3 3 2 1 0 11 10	Synchronous output type 1. Cs2:+4. Clock for both synchronous outputs. Synchronous output type 2. Cs2:+8.				
8 9 10 11 12 13 18 19	connected to: PA Output, limit L PA Output, limit L	1 765432 10	0 15 14 13 12 11 10 9 8	2,3 3 2 1 0 11 10 9 8	Synchronous output type 1. Cs2:+4. Clock for both synchronous outputs. Synchronous output type 2. Cs2:+8.				
8 9 10 11 12 13 18 19 2	connected to: PA Output, limit L PA Output, limit L	1 7 6 5 4 3 2 1 0	0 15 14 13 12 11 10 9 8	2,3 3 2 1 0 11 10 9 8	Synchronous output type 1. Cs2:+4. Clock for both synchronous outputs. Synchronous output type 2. Cs2:+8.				
8 9 10 11 12 13 18 19 2 15	connected to: PA Output, limit L PA Output, limit L RD2 Opto isolated +RTS2 Opto isolated	1 7 6 5 4 3 2 1 0 input.	0 15 14 13 12 11 10 9 8 Receiv	2,3 3 2 1 0 11 10 9 8 ve Data	Synchronous output type 1. Cs2:+4. Clock for both synchronous outputs. Synchronous output type 2. Cs2:+8. a. Negative at rest. 5–9mA, 825ohm in series. Send. On (negative) not ready. <25V, <5mA, Von <1V.				
8 9 10 11 12 13 18 19 2 15 5	connected to: PA Output, limit L PA Output, limit L RD2 Opto isolated +RTS2 Opto isolated +RTS2 Opto isolated	1 7 6 5 4 3 2 1 0 input. I output output	0 15 14 13 12 11 10 9 8 Receiv t. Rea	2,3 3 2 1 10 9 8 ve Data dy To smit D	Synchronous output type 1. Cs2:+4. Clock for both synchronous outputs. Synchronous output type 2. Cs2:+8. a. Negative at rest. 5–9mA, 825ohm in series. Send. On (negative) not ready. <25V, <5mA, Von <1V. ata. On (negative) at rest. <25V, <5mA, Von <1V.				
8 9 10 11 12 13 18 19 2 5 4 16	connected to: PA Output, limit L PA Output, limit L RD2 Opto isolated +TX2 Opto isolated CTS2 (DTR) Opto is TARE Opto isolated	1 7 6 5 4 3 2 1 0 input. I output output solated	0 15 14 13 12 11 10 9 8 Receiv t. Rea . Tran: input. pegat	2,3 3 2 1 0 11 10 9 8 ve Data dy To smit D Clear ive po	Synchronous output type 1. Cs2:+4. Clock for both synchronous outputs. Synchronous output type 2. Cs2:+8. a. Negative at rest. 5–9mA, 825ohm in series. Send. On (negative) not ready. <25V, <5mA, Von <1V. ata. On (negative) at rest. <25V, <5mA, Von <1V. To Send. Negative not ready. 5–9mA, 825ohm in series. arity. 5–9mA, 825ohm in series.				
8 9 10 11 12 13 18 19 2 15 5 4 16 24	connected to: PA Output, limit L PA Output, limit L RD2 Opto isolated +TX2 Opto isolated CTS2 (DTR) Opto iso TARE Opto isolated PRINT/TEST Opto i	1 7 6 5 4 3 2 1 0 input. I output solated input, solated	0 15 14 13 12 11 10 9 8 Receiv t. Rea . Trans input. negat d input	2,3 3 2 1 0 11 10 9 8 ve Data dy To smit D Clear ive po , nega	Synchronous output type 1. Cs2:+4. Clock for both synchronous outputs. Synchronous output type 2. Cs2:+8. a. Negative at rest. 5–9mA, 825ohm in series. Send. On (negative) not ready. <25V, <5mA, Von <1V. ata. On (negative) at rest. <25V, <5mA, Von <1V. To Send. Negative not ready. 5–9mA, 825ohm in series. larity. 5–9mA, 825ohm in series. tive polarity. 5–9mA, 825ohm in series.				
8 9 10 11 12 13 18 19 2 5 4 10 24 24	connected to: PA Output, limit L PA Output, limit L RD2 Opto isolated +RTS2 Opto isolated +TX2 Opto isolated CTS2 (DTR) Opto isolated PRINT/TEST Opto i U1272 only. PB4 Output	1 7 6 5 4 3 2 1 0 input. I output solated input, solated input, solated	0 15 14 13 12 11 10 9 8 Receiv t. Rea . Tran: . Input. negat d input or sele	2,3 3 2 1 1 10 9 8 ve Data dy To smit D Clear ive po c, nega cted tr	Synchronous output type 1. Cs2:+4. Clock for both synchronous outputs. Synchronous output type 2. Cs2:+8. a. Negative at rest. 5–9mA, 825ohm in series. Send. On (negative) not ready. <25V, <5mA, Von <1V. ata. On (negative) at rest. <25V, <5mA, Von <1V. To Send. Negative not ready. 5–9mA, 825ohm in series. larity. 5–9mA, 825ohm in series. tive polarity. 5–9mA, 825ohm in series. ansducer. Low is transducer 1.				
8 9 10 11 12 13 18 19 2 15 5 4 16 24 24 1 14	connected to: PA Output, limit L PA Output, limit L RD2 Opto isolated +TX2 Opto isolated CTS2 (DTR) Opto isolated PRINT/TEST Opto isolated PRINT/TEST Opto isolated PRINT/TEST Opto isolated Common return for Common return for	1 7 6 5 4 3 2 1 0 input. l output solated input, solated input, solated pins 2, pins 5	0 15 14 13 12 11 10 9 8 Receiv t. Rea . Tran: . Tran: . Input. negat d input sele 4, (16 and 15	2,3 3 2 1 0 11 10 9 8 ve Data dy To smit D Clear ive po c, nega cted tr 3 and 2 5.	Synchronous output type 1. Cs2:+4. Clock for both synchronous outputs. Synchronous output type 2. Cs2:+8. a. Negative at rest. 5–9mA, 825ohm in series. Send. On (negative) not ready. <25V, <5mA, Von <1V. ata. On (negative) at rest. <25V, <5mA, Von <1V. To Send. Negative not ready. 5–9mA, 825ohm in series. larity. 5–9mA, 825ohm in series. tive polarity. 5–9mA, 825ohm in series. ansducer. Low is transducer 1. 24 when J6 is not inserted).				
8 9 10 11 12 13 18 19 2 15 5 4 16 24 24 14 22	connected to: PA Output, limit L PA Output, limit L RD2 Opto isolated +RTS2 Opto isolated CTS2 (DTR) Opto isolated CTS2 (DTR) Opto isolated PRINT/TEST Opto i U1272 only. PB4 Opto Common return for PB Output overrang PB Output overrang	1 7 6 5 4 3 2 1 0 input. I output solated input, so	0 15 14 13 12 11 10 9 8 Receiv t. Rea . Trans input. negat d input sele 4, (16 and 15 buzze	2,3 3 2 1 0 11 10 9 8 ve Data dy To smit D Clear ive po cred tr cred tr and 2 5. r.	Synchronous output type 1. Cs2:+4. Clock for both synchronous outputs. Synchronous output type 2. Cs2:+8. a. Negative at rest. 5–9mA, 825ohm in series. Send. On (negative) not ready. <25V, <5mA, Von <1V. ata. On (negative) at rest. <25V, <5mA, Von <1V. To Send. Negative not ready. 5–9mA, 825ohm in series. larity. 5–9mA, 825ohm in series. tive polarity. 5–9mA, 825ohm in series. ansducer. Low is transducer 1. 24 when J6 is not inserted).				
8 9 10 11 12 13 18 19 2 15 5 4 16 24 14 22 23 7	connected to: PA Output, limit L PA Output, limit L RD2 Opto isolated +TX2 Opto isolated CTS2 (DTR) Opto is TARE Opto isolated PRINT/TEST Opto i U1272 only. PB4 Of Common return for PB Output overrang PB Output motion. CA1 Buffered edge	1 7 6 5 4 3 2 1 0 input. I output solated input, solated input, solated pins 2, pins 5 e and triage	0 15 14 13 12 11 10 9 8 Received input. negat d input. negat d input. received 4, (16 and 15 buzze red State	2,3 3 2 1 10 9 8 ve Data dy To smit D Clear ive po cted tr 5 and 2 5. r.	Synchronous output type 1. Cs2:+4. Clock for both synchronous outputs. Synchronous output type 2. Cs2:+8. a. Negative at rest. 5–9mA, 825ohm in series. Send. On (negative) not ready. <25V, <5mA, Von <1V. ata. On (negative) at rest. <25V, <5mA, Von <1V. To Send. Negative not ready. 5–9mA, 825ohm in series. larity. 5–9mA, 825ohm in series. tive polarity. 5–9mA, 825ohm in series. ansducer. Low is transducer 1. 24 when J6 is not inserted).				
8 9 10 11 12 13 18 19 2 15 5 4 16 24 14 223 7 6	connected to: PA Output, limit L PA Output, limit L RD2 Opto isolated +TX2 Opto isolated CTS2 (DTR) Opto is TARE Opto isolated PRINT/TEST Opto i U1272 only. PB4 Output overrang PB Output overrang PB Output motion. CA1 Buffered, edge CA2	1 7 6 5 4 3 2 1 0 input. I output solated input, solated input, solated pins 2, pins 5 e and trigge	0 15 14 13 12 11 10 9 8 Received t. Rea . Trans input. negat d input. red sw	2,3 3 2 1 0 11 10 9 8 ve Date dy To smit D Clear ive po c, nega cted tr b and 2 5. r.	Synchronous output type 1. Cs2:+4. Clock for both synchronous outputs. Synchronous output type 2. Cs2:+8. a. Negative at rest. 5–9mA, 825ohm in series. Send. On (negative) not ready. <25V, <5mA, Von <1V. ata. On (negative) at rest. <25V, <5mA, Von <1V. To Send. Negative not ready. 5–9mA, 825ohm in series. larity. 5–9mA, 825ohm in series. tive polarity. 5–9mA, 825ohm in series. ansducer. Low is transducer 1. 24 when J6 is not inserted).				
8 9 10 11 12 13 18 19 2 15 5 4 16 24 14 22 3 7 6 3 5	connected to: PA Output, limit L PA Output, limit L RD2 Opto isolated +RTS2 Opto isolated CTS2 (DTR) Opto is TARE Opto isolated PRINT/TEST Opto i U1272 only. PB4 Output overrang PB Output overrang PB Output motion. CA1 Buffered, edge CA2 CB1 CB2 Output overrang	1 7 6 5 4 3 2 1 0 input. I output solated input, solated input, solated input, solated input, solated input, solated input, solated input, solated input, solated	0 15 14 13 12 11 10 9 8 Received t. Rea . Trans input. negat d input. red sw	2,3 3 2 1 0 11 0 9 8 ve Data dy To smit D Clear ive po cted tr clear ive po c, nega cted tr clear ive po f, nega cted tr c smit D	Synchronous output type 1. Cs2:+4. Clock for both synchronous outputs. Synchronous output type 2. Cs2:+8. a. Negative at rest. 5–9mA, 825ohm in series. Send. On (negative) not ready. <25V, <5mA, Von <1V. ata. On (negative) at rest. <25V, <5mA, Von <1V. To Send. Negative not ready. 5–9mA, 825ohm in series. larity. 5–9mA, 825ohm in series. tive polarity. 5–9mA, 825ohm in series. ansducer. Low is transducer 1. 24 when J6 is not inserted).				
8 9 10 11 12 13 18 19 2 15 5 4 6 24 14 22 3 7 6 3 25 17	connected to: PA Output, limit L PA Output, limit L RD2 Opto isolated +RTS2 Opto isolated +TX2 Opto isolated CTS2 (DTR) Opto is TARE Opto isolated PRINT/TEST Opto i U1272 only. PB4 Opto Common return for PB Output overrang PB Output motion. CA1 Buffered, edge CA2 CB1 CB2 Output overrang Ground	1 7 6 5 4 3 2 1 0 input. I output solated input, solated input, solated input fo pins 2, pins 5 e and trigge ge.	0 15 14 13 12 11 10 9 8 Receiv t. Rea . Trans . Trans . Trans . Trans . Trans t input . Trans . Trans	2,3 3 2 1 0 11 10 9 8 ve Data dy To smit D Clear ive po , nega cted tr 5 and 2 5. r.	Synchronous output type 1. Cs2:+4. Clock for both synchronous outputs. Synchronous output type 2. Cs2:+8. a. Negative at rest. 5–9mA, 825ohm in series. Send. On (negative) not ready. <25V, <5mA, Von <1V. ata. On (negative) at rest. <25V, <5mA, Von <1V. To Send. Negative not ready. 5–9mA, 825ohm in series. larity. 5–9mA, 825ohm in series. tive polarity. 5–9mA, 825ohm in series. ansducer. Low is transducer 1. 24 when J6 is not inserted).				
$\begin{array}{c} 8\\ 9\\ 10\\ 11\\ 12\\ 13\\ 18\\ 19\\ 2\\ 15\\ 5\\ 4\\ 16\\ 24\\ 14\\ 223\\ 7\\ 6\\ 3\\ 25\\ 17\\ 21\end{array}$	connected to: PA Output, limit L PA Output, limit L RD2 Opto isolated +TX2 Opto isolated CTS2 (DTR) Opto is TARE Opto isolated PRINT/TEST Opto i U1272 only. PB4 Out Common return for PB Output overrang PB Output overrang PB Output overrang CA2 CB1 CB2 Output overrang Ground. 0 (Digital zero).	1 7 6 5 4 3 2 1 0 input. contput solated input, solated input, solated pins 5 e and trigge ge.	0 15 14 13 12 11 10 9 8 Receiv t. Rea . Trans l input. negat d input or sele 4, (16 and 19 buzze red sw	2,3 3 2 1 0 11 10 9 8 ve Data dy To smit D Clear ive po c, nega cted tr 5 and 2 5. r.	Synchronous output type 1. Cs2:+4. Clock for both synchronous outputs. Synchronous output type 2. Cs2:+8. a. Negative at rest. 5–9mA, 825ohm in series. Send. On (negative) not ready. <25V, <5mA, Von <1V. ata. On (negative) at rest. <25V, <5mA, Von <1V. To Send. Negative not ready. 5–9mA, 825ohm in series. larity. 5–9mA, 825ohm in series. tive polarity. 5–9mA, 825ohm in series. ansducer. Low is transducer 1. 24 when J6 is not inserted).				
$\begin{array}{c} 8 \\ 9 \\ 10 \\ 11 \\ 12 \\ 13 \\ 19 \\ 2 \\ 15 \\ 5 \\ 4 \\ 16 \\ 24 \\ 14 \\ 22 \\ 3 \\ 7 \\ 6 \\ 3 \\ 5 \\ 17 \\ 20 \end{array}$	connected to: PA Output, limit L PA Output, limit L RD2 Opto isolated +RTS2 Opto isolated +TX2 Opto isolated CTS2 (DTR) Opto is TARE Opto isolated PRINT/TEST Opto i U1272 only. PB4 Output overrang PB Output overrang PB Output motion. CA1 Buffered, edge CA2 CB1 CB2 Output overrang Ground. 0 (Digital zero). +5 Volt.	1 7 6 5 4 3 2 1 0 input. I output solated input, solated input, solated input, solated input, solated input, solated input, solated input, solated pins 5 e and trigge ge.	0 15 14 13 12 11 10 9 8 Receiv t. Rea . Trans input. negat d input sele 4, (16 and 15 buzze red sw	2,3 3 2 1 0 11 9 8 ve Data dy To smit D Clear ive po cred tr clear ive po cred tr and 2 5. r.	Synchronous output type 1. Cs2:+4. Clock for both synchronous outputs. Synchronous output type 2. Cs2:+8. a. Negative at rest. 5–9mA, 825ohm in series. Send. On (negative) not ready. <25V, <5mA, Von <1V. ata. On (negative) at rest. <25V, <5mA, Von <1V. To Send. Negative not ready. 5–9mA, 825ohm in series. arity. 5–9mA, 825ohm in series. tive polarity. 5–9mA, 825ohm in series. ansducer. Low is transducer 1. 24 when J6 is not inserted). r external transducer selection.				

Conditions for non opto isolated PIA signals. Never use voltages higher than the internal +5V or below 0. **Inputs:** 

UNI SYSTEM sweden www.unisystem.se	Unisystem AB Stockby Hantverksby 19 S-181 75 LIDINGÖ (Seat)	<b>Tel.:</b> +46-87679280 <b>Fax:</b> +46-87678310 lidingo@unisystem.se	Factory: Bergebyv. 24 S-685 29 TORSBY	<b>Tel.:</b> +46-56014055 <b>Fax:</b> +46-56010125 torsby@unisystem.se
Outputs: PB, CB2.	V out = 1.5\	/ I out min –1mA, max	–10mA.	
PA, CA2.	V in = 2.4V V in = 0.4V	I in min  –0.2mA. I in max  –2.4mA.		
PB, CA1, CB1, CB2	2.	I in max ±10uA.		